A1 Background of the Invention:

Field of the Invention: --.

Page 2, between lines 10 and 12, insert

A2 -- Summary of the Invention: --;

Page 3, between lines 27 and 29, insert

A3 -- Brief Description of the Drawings: --;

Page 4 above the first line, insert

A4 -- Description of the Preferred Embodiment: --.

Page 9, line 1, change "Patent Claims" to -- We Claim: --.

In the Claims:

Sub B1 Cancel claims 1-10 and enter the following new claims.

--11. A controllable current source circuit, comprising:

A5 an output;

supply voltage terminals;

first and second driver stages connected in series between said voltage supply terminals and having a mutual junction point connected to said output;

wherein only said first driver stage switches on and off in dependence on an input signal, and said second driver stage is switched on and carries a stabilized current.

12. The current source circuit according to claim 11, wherein said first driver stage forms a part of a current mirror circuit receiving a flow of a stabilized current when said first driver stage is switched on.

13. The current source circuit according to claim 11, wherein said second driver stage forms a part of a current mirror circuit causing a constant stabilized current to flow in said second driver stage.

14. The current source circuit according to claim 13, wherein said current mirror circuit is coupled to a current mirror circuit connected to said first driver stage and causes a stabilized current to flow in said current mirror circuit connected to said first driver stage.

15. The current source circuit according to claim 13, wherein a current carried by said first driver stage when said first driver stage is switched on is greater than a current carried by said second driver stage.

5 ~~16.~~ The current source circuit according to claim ~~15,~~ 9 wherein the current carried in said first driver stage is multiple times greater than the current carried by said second driver stage.

6 ~~17.~~ The current source circuit according to claim ~~15,~~ 4 wherein the current carried in said first driver stage is four times greater than the current carried by said second driver stage.

18. A phase locked loop, comprising:

a phase comparator having a phase comparison circuit with a reference signal input for receiving a reference signal and an input for receiving an input signal whose phase angle is to be regulated, and having a current source circuit according to claim 1 on an output side of said phase comparator;

a loop filter connected to said current source circuit and having an output for outputting an output signal controlling the phase angle of the input signal.

8 ~~19.~~ The phase locked loop according to claim ~~18,~~ 7 wherein said phase comparison circuit contains a comparator configured to switch between two output states and having a single output terminal connected to said current source circuit.